

IN THE CLAIMS:

Please amend the claims as follows:

Claim 1 (Currently Amended): A monitoring circuit, comprising:
a capacitor[,];
a charging means for charging circuit adapted to charge the capacitor[,,];
a first discharging means for discharging circuit adapted to discharge the capacitor[,,];
a voltage comparing means for comparing circuit adapted to compare a certain reference
voltage with a charging charged voltage of the capacitor and to output a first reset signal to ,
thereby generating a signal for resetting an operation of a monitoring object when the capacitor is
charged to have a certain voltage or more[,];
a and source voltage deciding means for monitoring detect circuit adapted to monitor a
source voltage of the monitoring object and to output a second reset signal to resetting the
operation of the monitoring object when the source voltage of the monitoring object is equal to or
lower than a certain voltage; and
a logic circuit to which the first reset signal and the second reset signal are inputted and
controls the charging circuit and the discharging circuit.

Claim 2 (Currently Amended): The A monitoring circuit according to claim 1, further comprising, a second capacitor, charging means for charging the capacitor, first discharging means for discharging the capacitor, second discharging circuit adapted to discharge means for discharging the capacitor in response to the first reset signal. [[,]] voltage comparing means for comparing a certain reference voltage with a voltage of the capacitor, thereby generating a signal for resetting an operation of a monitoring object when the capacitor is charged to have a certain voltage or more, and source voltage deciding means for monitoring a source voltage of the monitoring object and resetting the operation of the monitoring object when the source voltage of the monitoring object is equal to or lower than a certain voltage.

Claim 3 (Currently Amended): The monitoring circuit according to claim 1 or 2, wherein the source voltage deciding means detected circuit is constituted by a comparator and the charging means circuit is constituted by a current source.

Claim 4 (Currently Amended): The monitoring circuit according to claim 2, wherein the first discharging means circuit is constituted by a current source and the second discharging means circuit is constituted by an analog switch, and either or both of the first and second discharging means circuit is/are operated to invert an output of the voltage comparing means circuit, thereby resetting the monitoring object when the source voltage of the monitoring object is equal to or lower than a certain voltage.

Claim 5 (Currently Amended): The monitoring circuit according to claim 2, wherein the first discharging means circuit is constituted by a current source and the second discharging means circuit is constituted by an N-type MOS transistor.

Claim 6 (Currently Amended): The monitoring circuit according to claim 1 or 2, wherein the voltage comparing means circuit is constituted by a window comparator.

Claim 7 (Currently Amended): The monitoring circuit according to claim 1 or 2, wherein the voltage comparing means circuit is constituted by a hysteresis comparator.

Claim 8 (Original): A semiconductor device comprising the monitoring circuit according to claim 1 or 2 in which the capacitor is provided on an outside of the semiconductor device.